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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ko, *et al.* Docket No.: TSM03-0615  
Serial No.: 10/729,095 Art Unit: 2818  
Filed: December 5, 2003 Examiner: TBD  
For: Structure and Method of a Strained Channel Transistor and a Second  
Semiconductor Component in an Integrated Circuit

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Date of Deposit: October 8, 2004

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Certificate of Mailing via First Class Mail (1 page)  
Information Disclosure Statement (1 page)  
IDS Form PTO/SB/08a and 08b (2 pages) citing (15) references  
Copies of (5) cited references  
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Respectfully submitted,

*Natalie Swider*

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Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a & 08b that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

October 8, 2004

Date

Respectfully submitted,

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*(Use as many sheets as necessary)*

Application Number	10/729,095
Filing Date	December 5, 2003
First Named Inventor	Ko, <i>et al.</i>
Art Unit	2818
Examiner Name	TBD
Attorney Docket Number	TSM03-0615

Sheet	1	of	2
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TSM03-0615

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